EN: This Datasheet is presented by the manufacturer.

Please visit our website for pricing and availability at www.hestore.hu.

REG012864AYPP3N00000

SPECIFICATION

CUSTOMER:

APPROVED BY	
PCB VERSION	
DATE	

FOR CUSTOMER USE ONLY

SALES BY	APPROVED BY	CHECKED BY	PREPARED BY

ISSUED DATE:

Contents

1. Revision History

DATE	VERSION	REVISED PAGE NO.	Note
2009/11/05	1		First issue

2. General Specification

The Features is described as follow:

- Module dimension: 89.7 × 47.2 × 3.4 (max.) mm^3

- View area: 63.41 × 32.69 mm^2

- Active area: 61.41 × 30.69 mm^2

- Number of dots: 128 x 64

- Pixel size: 0.45 x 0.45 mm^2

- Pixel pitch: 0.48 x 0.48 mm^2

- Module type: Graphic, Passive Matrix

- Duty: 1/64

- IC Package: TCP type

- Emitting Color: Yellow

- Brightness: 100 cd/m^2

- Contrast: 2000:1

3. Module Coding System

R	E	G	012864	A	Y	P	P	3	N	00000
1	2	3	4	5	6	7	8	9	10	11

Item	Description		
1	R：Raystar Optronics Inc.		
2	E：OLED		
3	Display Type	C：Character	
		G：Graphic	
4	Number of dots：**128 x64 Dots**		
5	Serials code		
6	Emitting Color	A：Amber	R：RED
		B：Blue	**Y：Yellow**
		G：Green	W：White
7	Polarizer	**P：With Polarizer;** N: Without Polarizer	
8	Display Mode	**P：Passive Matrix ;** A: Action Matrix	
9	Driver Voltage	**3：3.0 V;** 5: 5.0 V	
10	Touch Panel	**N：Without touch panel;** S: With touch panel	
11	Serial No.	00000: Sales code	

4. Interface Pin Function

No.	Symbol	I/O	Function
1	NC(GND)		Reserved Pin (Supporting Pin) The supporting pin can reduce the influences from stresses on the function pins. This pin must be connected to external ground.
2	VCC	P	Power Supply for OLED Panel This is the most positive voltage supply pin of the chip. It must be supplied externally.
3	VCOMH	P	Voltage Output High Level for COM Signal This pin is the input pin for the voltage output high level for COM signals. It can be supplied externally or internally. When VCOMH is generated internally, a capacitor should be connected between this pin and VSS.
4	IREF	I	Current Reference for Brightness Adjustment This pin is segment current reference pin. A resistor should be connected between this pin and VSS. Set the current at 10µA.
5~12	D7~D0	I/O	Host Data Input/Output Bus These pins are 8-bit bi-directional data bus to be connected to the microprocessor's data bus. When serial mode is selected, D1 will be the serial data input SDIN and D0 will be the serial clock input SCLK
13	E/RD#	I	Read/Write Enable or Read This pin is MCU interface input. When interfacing to a 68XX-series microprocessor, this pin will be used as the Enable (E) signal. Read/write operation is initiated when this pin is pulled high and the CS# is pulled low. When connecting to an 80XX-microprocessor, this pin receives the Read (RD#) signal. Data read operation is initiated when this pin is pulled low and CS# is pulled low.
14	R/W#	I	Read/Write Select or Write This pin is MCU interface input. When interfacing to a 68XX-series microprocessor, this pin will be used as Read/Write (R/W#) selection input. Pull this pin to "High" for read mode and pull it to "Low" for write mode. When 80XX interface mode is selected, this pin will be the Write (WR#) input. Data write operation is initiated when this pin is pulled low and the CS# is pulled low.

15	D/C#	I	Data/Command Control This pin is Data/Command control pin. When the pin is pulled high, the input at D7~D0 is treated as display data. When the pin is pulled low, the input at D7~D0 will be transferred to the command register. For detail relationship to MCU interface signals, please refer to the Timing Characteristics Diagrams. When the pin is pulled high and serial interface mode is selected, the data at SDIN is treated as data. When it is pulled low, the data at SDIN will be transferred to the command register.
16	RES#	I	Power Reset for Controller and Driver This pin is reset signal input. When the pin is low, initialization of the chip is executed.
17	CS#	I	Chip Select This pin is the chip select input. The chip is enabled for MCU communication only when CS# is pulled low.
18	NC		Reserved Pin The N.C. pins between function pins are reserved for compatible and flexible design.
19	BS2	I	Communicating Protocol Select These pins are MCU interface selection input. See the following table:
20	BS1		
21	Vdd	P	Power Supply for Logic Circuit This is a voltage supply pin. It must be connected to external source.
22	NC		
23	NC		
24	NC		Reserved Pin
25	NC		The N.C. pins between function pins are reserved for
26	NC		compatible and flexible design.
27	NC		
28	NC		
29	Vss	P	Ground of OLED System This is a ground pin. It also acts as a reference for the logic pins, the OLED driving voltages, and the analog circuits. It must be connected to external ground.
30	VSL	O	Voltage Output Low Level for SEG Signal This pin is the output pin for the voltage output low level for SEG signals. A capacitor should be connected between this pin and VSS.

Table within rows 19/20:

	68XX-parallel	80XX-parallel	Serial
BS1	0	1	0
BS2	1	1	0

5. Outline Dimension

PIN NO.	SYMBOL	PIN NO.	SYMBOL
1	NC	16	RES#
2	VCC	17	CS#
3	VCOMH	18	NC
4	IREF	19	BS2
5	D7	20	BS1
6	D6	21	VDD
7	D5	22	NC
8	D4	23	NC
9	D3	24	NC
10	D2	25	NC
11	D1	26	NC
12	D0	27	NC
13	E/RD#	28	NC
14	R/W#	29	VSS
15	D/C#	30	VSL

DOT SIZE
SCALE 10/1

SIZE A
SCALE 2/1

W=0.35
P0.5*29=14.5

CONTCT SIZE

The non-specified tolerance of dimension is ±0.2mm.

Note:
1.1/64DUTY,VDD=2.8V,VCC=15.0V
2.Controllar IC=SSD1325
3.LCD TYPE:TAB
  Operating:-30~70/Storange:-40~80
4.Interface:
  8 bit 68xx/80xx Parallel,Serial interface

6. Optics & Electrical Characteristics

6.1 Optics Characteristics

Characteristics	Symbol	Condition	Min	Typ	Max	Unit
Brightness	Lbr	With Polarizer (Note 3)	70	100	—	cd/m2
C.I.E. (Yellow)	(x) (y)	Without Polarizer	0.44 0.46	0.48 0.50	0.52 0.54	
Dark Room Contrast	CR		—	>2000:1	—	degree
View Angle			>160	—	—	

* **Optical measurement taken at V_{DD} = 2.8V, V_{CC} = 15V.**

Software configuration follows Section 4.4 Initialization.

6.2 DC Characteristics

Characteristics	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage for Logic	VDD		2.4	2.8	3.5	V
Supply Voltage for Display	VCC	Note 3	14.25	15	15.75	V
High Level Input	VIH	Iout = 100µA,3.3MHz	0.8×VDD	—	VDD	V
Low Level Input	VIL	Iout = 100µA,3.3MHz	0	—	0.2×VDD	V
High Level Output	VOH	Iout =100µA,3.3MHZ	0.9×VDD	—	VDD	V
Low Level Input	VOL	Iout =100µA,3.3MHZ	0	—	0.1×VDD	V
Operating Current for VDD	IDD	Note 4	—	250	400	µA
		Note 5	—	250	400	µA
Operating Current for VCC	ICC	Note 4	—	31	39	mA
		Note 5	—	53	66	mA
Sleep Mode Current for VDD	IDD, SLEEP		—	1	5	µA
Sleep Mode Current for VCC	ICC, SLEEP		—	1	5	µA

Note 3: Brightness (L_{br}) and Supply Voltage for Display (V_{CC}) are subject to the change of the panel characteristics and the customer's request.

Note 4: V_{DD} = 2.8V, V_{CC} = 15V, 50% Display Area Turn on.

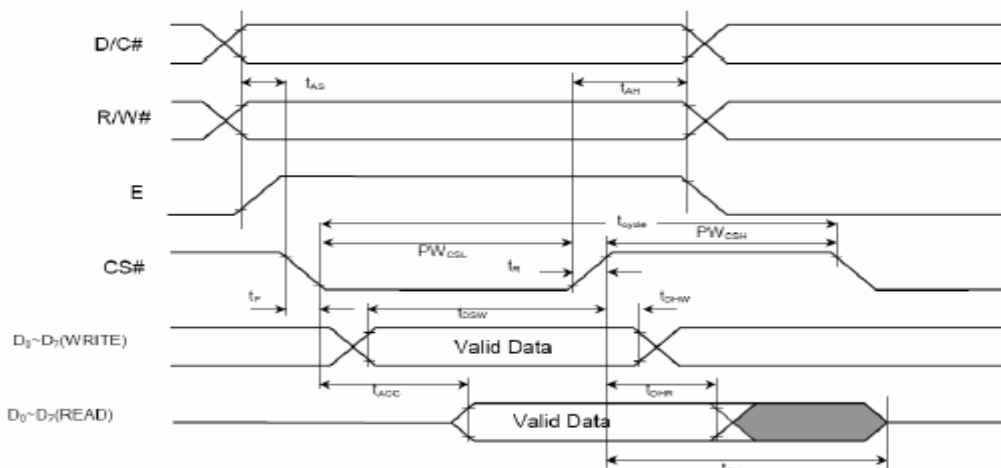Note 5: V_{DD} = 2.8V, V_{CC} = 15V, 100% Display Area Turn on.

* Software configuration follows Section 4.4 Initialization.

6.3 AC Characteristics

6.3.1 68XX-Series MPU Parallel Interface Timing Characteristics:

Symbol	Description	Min	Max	Unit
t_{cycle}	System Cycle Time	300	—	ns
t_{AS}	Address Setup Time	0	—	ns
t_{AH}	Address Hold Time	0	—	ns
t_{DSW}	Write Data Setup Time	40	—	ns
t_{DHW}	Write Data Hold Time	15	—	ns
t_{DHR}	Read Data Hold Time	20	—	ns
t_{OH}	Output Disable Time	—	70	
t_{ACC}	Access Time	—	140	ns
PW_{CSL}	Chip Select Low Pulse Width (Read) Chip Select Low Pulse width (Write)	120 60	—	ns
PW_{CSH}	Chip Select High Pulse Width (Read) Chip Select High Pulse Width (Write)	60 60	—	ns
t_R	Rise Time	—	15	ns
t_F	Fall Time	—	15	ns

* (V_{DD} - V_{SS} = 2.4 V to 3.5V, T_a = 25°C)

6.3.2 80XX-Series MPU Parallel Interface Timing Characteristics:

Symbol	Description	Min	Max	Unit
t_{cycle}	Clock Cycle Time	300	-	ns
t_{AS}	Address Setup Time	0	-	ns
t_{AH}	Address Hold Time	0	-	ns
t_{DSW}	Write Data Setup Time	40	-	ns
t_{DHW}	Write Data Hold Time	15	-	ns
t_{DHR}	Read Data Hold Time	20	-	ns
t_{OH}	Output Disable Time	-	70	ns
t_{ACC}	Access Time	-	140	ns
PW_{CSL}	Chip Select Low Pulse Width (Read) Chip Select Low Pulse width (Write)	120 60	-	ns
PW_{CSH}	Chip Select High Pulse Width (Read) Chip Select High Pulse Width (Write)	60 60	-	ns
t_R	Rise Time	-	15	ns
t_F	Fall Time	-	15	ns

* (V_{DD} - V_{SS} = 2.4 V to 3.5V, T_a = 25°C)

6.3.3 Serial Interface Timing Characteristics:

Symbol	Description	Min	Max	Unit
t_{cycle}	Clock Cycle Time	250	—	ns
t_{AS}	Address Setup Time	150	—	ns
t_{AH}	Address Hold Time	150	—	ns
t_{CSS}	Chip Select Setup Time	120	—	ns
t_{CSH}	Chip Select Hold Time	60	—	ns
t_{DSW}	Write Data Setup Time	100	—	ns
t_{DHW}	Write Data Hold Time	100	—	ns
t_{CLKL}	Serial Clock Low Time	100	—	ns
t_{CLKH}	Serial Clock High Time	100	—	ns
t_R	Rise Time	—	15	ns
t_F	Fall Time	—	15	ns

* (V_{DD} - V_{SS} = 2.4 V to 3.5V, T_a = 25°C)
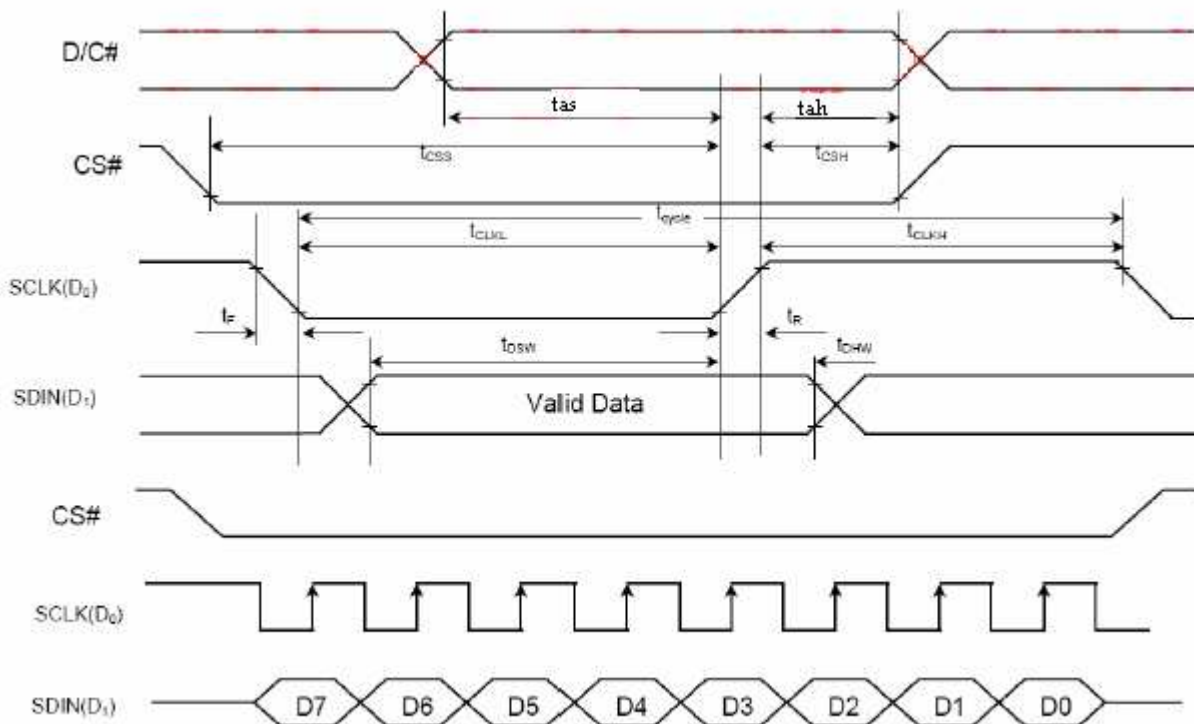
7. Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit	Notes
Supply Voltage for Logic	VDD	-0.3	4	V	1,2
Supply Voltage for Display	VCC	0	16	V	1,2
Operating Temperature	TOP	-30	70	°C	—
Storage Temperature	TSTG	-40	80	°C	—

Note 1: All the above voltages are on the basis of "VSS = 0V".

Note 2: When this module is used beyond the above absolute maximum ratings, permanent breakage of the module may occur. Also, for normal operations, it is desirable to use this module under the conditions according to Section 3. "Optics & Electrical Characteristics". If this module is used beyond these conditions, malfunctioning of the module can occur and the reliability of the module may deteriorate.

8. Block Diagram
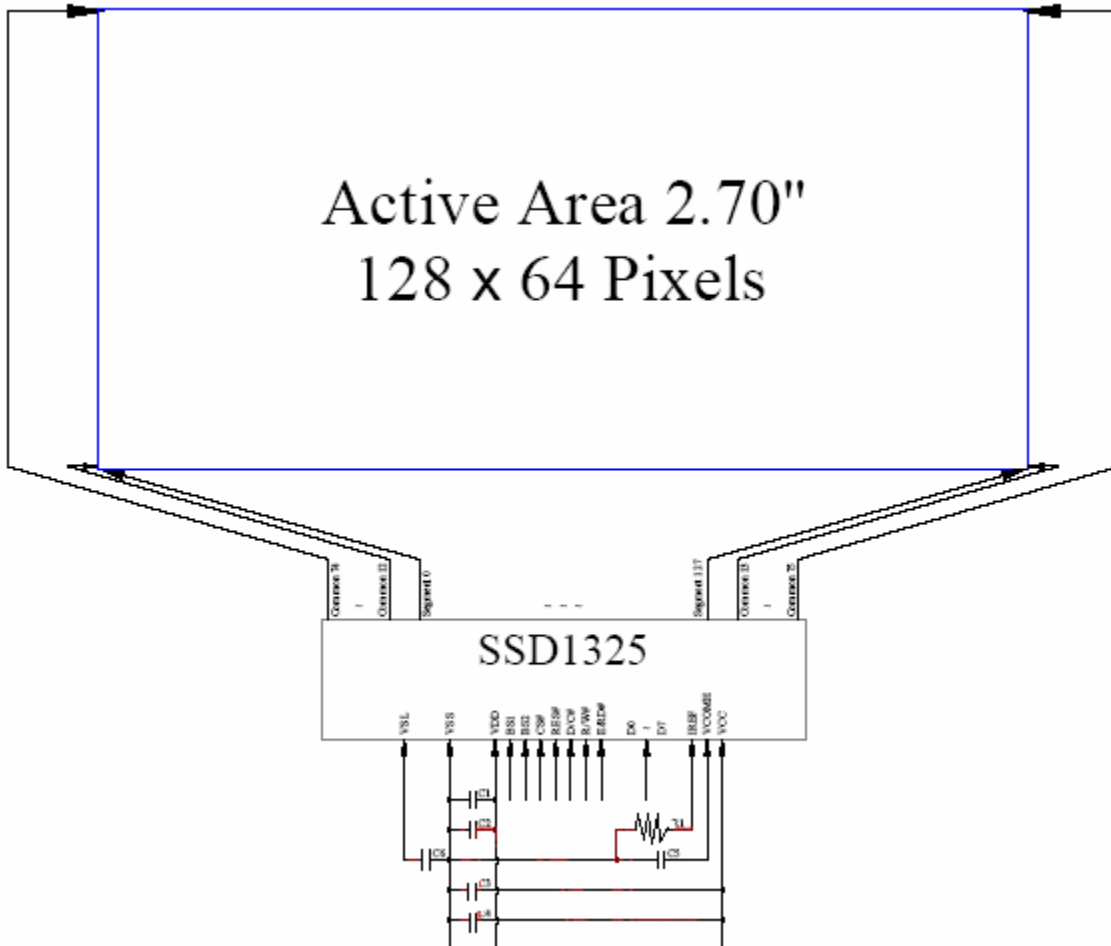
MCU Interface Selection: BS1 and BS2

Pins connected to MCU interface: CS#, RES#, D/C#, R/W#, E/RD#, and D0~D7

C1, C3: 0.1μF

C2, C6: 4.7μF

C4: 10μF

C5: 4.7μF / 25V Tantalum Capacitor

R1: 820kΩ, R1 = (Voltage at IREF - VSS) / IREF

9. Reliability

9.1 Contents of Reliability Tests

Item	Conditions	Criteria
High Temperature Operation	70℃,240hrs	
Low Temperature Operation	-30℃,240hrs	The operational functions work.
High Temperature Storage	80℃,240hrs	
Low Temperature Storage	-40℃,240hrs	
High Temperature/Humidity Operation/ Thermal Shock	60℃,90%RH,120hrs，-40℃85℃，24cycles 1 hr dwell	

* The samples used for the above tests do not include polarizer.

* No moisture condensation is observed during tests.

9.2 Lifetime
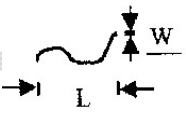
Parameter	Min	Max	Unit	Condition	Notes
Operating Life Time	40，000	—	Hrs	100 cd/m2, 50% Checkerboard	6
	55，000	—		75 cd/m2, 50% Checkerboard	
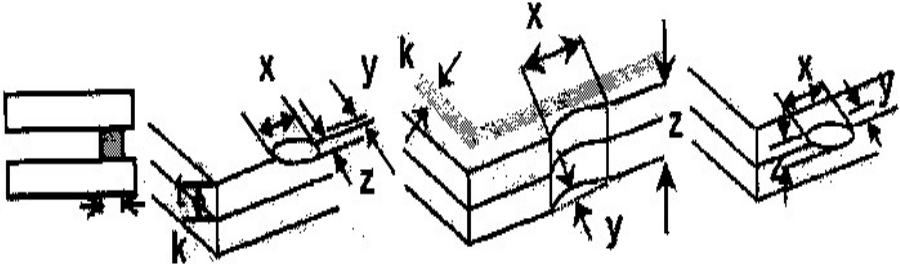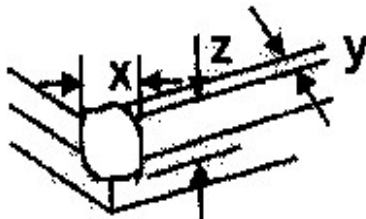
Note 6: The average operating lifetime at room temperature is estimated by the accelerated operation at high temperature conditions.
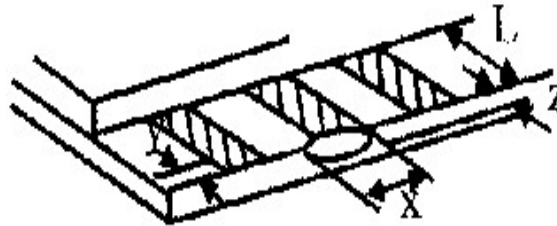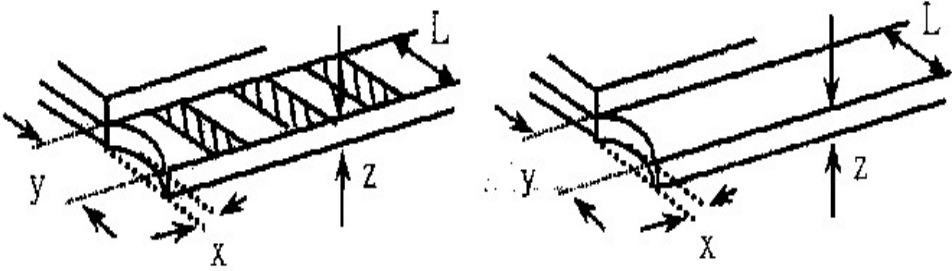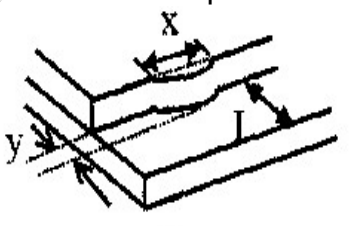
9.3 Failure Check Standard

After the completion of the described reliability test, the samples were left at room temperature for 2 hrs prior to conducting the failure test at 23±5°C; 55±15% RH.

10. Inspection specification

NO	Item	Criterion	AQL
01	Electrical Testing	1.1 Missing vertical, horizontal segment, segment contrast defect. 1.2 Missing character, dot or icon. 1.3 Display malfunction. 1.4 No function or no display. 1.5 Current consumption exceeds product specifications. 1.6 Viewing angle defect. 1.7 Mixed product types. 1.8 Contrast defect.	0.65
02	Black or bright spots on OLED (display only)	2.1 Bright and black spots on display $\leqq$ 0.25mm, no more than three Bright or black spots present. 2.2 Densely spaced: No more than two spots or lines within 3mm	2.5
03	Black spots, bright spots, contamination (non-display)	3.1 Round type : As following drawing $\Phi = (x + y) / 2$	2.5

3.2 Line type : (As following drawing)

Length	Width	Acceptable QTY
---	W $\leqq$ 0.02	Accept no dense
L $\leqq$ 3.0	0.02 < W $\leqq$ 0.03	2
L $\leqq$ 2.5	0.03 < W $\leqq$ 0.05	
---	0.05 < W	As round type

AQL: 2.5

| 04 | Polarizer bubbles | If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction. | 2.5 |

Size Φ	Acceptable QTY
$\Phi \leqq$ 0.20	Accept no dense
0.20 < $\Phi \leqq$ 0.50	3
0.50 < $\Phi \leqq$ 1.00	2
1.00 < Φ	0
Total QTY	3

NO	Item	Criterion	AQL
05	Scratches	Follow NO.3 black spots, bright spots, contamination	
06	Chipped glass	Symbols Define: x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: OLED side length L: Electrode pad length: 6.1 General glass chip : 6.1.1 Chip on panel surface and crack between panels:	2.5

z: Chip thickness	y: Chip width	x: Chip length
$Z \leqq 1/2t$	Not over viewing area	$x \leqq 1/8a$
$1/2t < z \leqq 2t$	Not exceed $1/3k$	$x \leqq 1/8a$

⊙If there are 2 or more chips, x is total length of each chip.

6.1.2 Corner crack:

z: Chip thickness	y: Chip width	x: Chip length
$Z \leqq 1/2t$	Not over viewing area	$x \leqq 1/8a$
$1/2t < z \leqq 2t$	Not exceed $1/3k$	$x \leqq 1/8a$

⊙If there are 2 or more chips, x is the total length of each chip.

NO	Item	Criterion	AQL
06	Glass crack	Symbols : x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: OLED side length L: Electrode pad length 6.2 Protrusion over terminal : 6.2.1 Chip on electrode pad :	2.5

6.2.1 Chip on electrode pad :

y: Chip width	x: Chip length	z: Chip thickness
$y \leqq 0.5mm$	$x \leqq 1/8a$	$0 < z \leqq t$

6.2.2 Non-conductive portion:

y: Chip width	x: Chip length	z: Chip thickness
$y \leqq L$	$x \leqq 1/8a$	$0 < z \leqq t$

⊙If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.

⊙If the product will be heat sealed by the customer, the alignment mark not be damaged.

6.2.3 Substrate protuberance and internal crack.

y: width	x: length
$y \leqq 1/3L$	$x \leqq a$

NO	Item	Criterion	AQL
07	Cracked glass	The OLED with extensive crack is not acceptable.	2.5
08	Bezel	8.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination. 8.2 Bezel must comply with job specifications.	2.5 0.65
9	PCB、COB	9.1 COB seal may not have pinholes larger than 0.2mm or contamination. 9.2 COB seal surface may not have pinholes through to the IC. 9.3 The height of the COB should not exceed the height indicated in the assembly diagram. 9.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places. 9.5 No oxidation or contamination PCB terminals. 9.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts. 9.7 The jumper on the PCB should conform to the product characteristic chart. 9.8 If solder gets on bezel tab pads, zebra pad or screw hold pad, make sure it is smoothed down. 9.9 The Scraping testing standard for Copper Coating of PCB **X * Y<=2mm^2**	2.5 2.5 0.65 2.5 2.5 0.65 0.65 2.5 2.5
10	Soldering	10.1 No un-melted solder paste may be present on the PCB. 10.2 No cold solder joints, missing solder connections, oxidation or icicle. 10.3 No residue or solder balls on PCB. 10.4 No short circuits in components on PCB.	2.5 2.5 2.5 0.65

NO	Item	Criterion	AQL
11	General appearance	11.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP.	2.5
		11.2 No cracks on interface pin (OLB) of TCP.	0.65
		11.3 No contamination, solder residue or solder balls on product.	2.5
		11.4 The IC on the TCP may not be damaged, circuits.	2.5
		11.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it causes the interface pin to sever.	2.5
		11.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color.	2.5
		11.7 Sealant on top of the ITO circuit has not hardened.	0.65
		11.8 Pin type must match type in specification sheet.	0.65
		11.9 OLED pin loose or missing pins.	0.65
		11.10 Product packaging must the same as specified on packaging specification sheet.	
		11.11 Product dimension and structure must conform to product specification sheet.	0.65

11. Precautions in use of Modules

1. Avoid applying excessive shocks to the module or making any alterations or modifications to it.
2. Don't make extra holes on the printed circuit board, modify its shape or change the components of OLED module.
3. Don't disassemble the OLED module.
4. Don't operate it above the absolute maximum rating.
5. Don't drop, bend or twist OLED module.
6. Soldering: only to the I/O terminals.
7. Storage: please storage in anti-static electricity container and clean environment.

12. Material List of Components for RoHs

1. RAYSTAR Optronics Inc., Ltd. hereby declares that all of or part of products, including, but not limited to, the Module, accessories or packages, manufactured and/or delivered to your company (including your subsidiaries and affiliated company) directly or indirectly by our company (including our subsidiaries or affiliated companies) do not intentionally contain any of the substances listed in all applicable EU directives and regulations, including the following substances.

   Exhibit A：The Harmful Material List

Material	(Cd)	(Pb)	(Hg)	(Cr6+)	PBBs	PBDEs
Limited Value	100 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm
Above limited value is set up according to RoHS.						

2. Process for RoHS requirement：
   (1) Use the Sn/Ag/Cu soldering surface; the surface of Pb-free solder is rougher than we used before.
   (2) Heat-resistance temp.：
       Reflow：250℃, 30 seconds Max.；
       Connector soldering wave or hand soldering：320℃, 10 seconds max.
   (3) Temp. curve of reflow, max. Temp.：235±5℃；
       Recommended customer's soldering temp. of connector：280℃, 3 seconds.

Module Sample Estimate Feedback Sheet

Module Number： _____

1、Panel Specification：

1. Panel Type：	□ Pass	□ NG , _____
2. Numbers of Pixel：	□ Pass	□ NG , _____
3. View Area：	□ Pass	□ NG , _____
4. Active Area：	□ Pass	□ NG , _____
5.Emitting Color：	□ Pass	□ NG , _____
6.Uniformity：	□ Pass	□ NG , _____
7.Operating Temperature：	□ Pass	□ NG , _____
8.Storage Temperature：	□ Pass	□ NG , _____
9.Others ： _____		

2、Mechanical Specification：

1. PCB Size：	□ Pass	□ NG , _____
2.Frame Size：	□ Pass	□ NG , _____
3.Materal of Frame：	□ Pass	□ NG , _____
4.Connector Position：	□ Pass	□ NG , _____
5.Fix Hole Position：	□ Pass	□ NG , _____
6. Thickness of PCB：	□ Pass	□ NG , _____
7. Height of Frame to PCB：	□ Pass	□ NG , _____
8.Height of Module：	□ Pass	□ NG , _____
9.Others ：	□ Pass	□ NG , _____

3、Relative Hole Size：

1.Pitch of Connector：	□ Pass	□ NG , _____
2.Hole size of Connector：	□ Pass	□ NG , _____
3.Mounting Hole size：	□ Pass	□ NG , _____
4.Mounting Hole Type：	□ Pass	□ NG , _____
5.Others ：	□ Pass	□ NG , _____

＞＞ **Go to page 2** ＜＜

Module Number： _____		
4、Electronic Characteristics of Module：		
1.Input Voltage：	□ Pass	□ NG , _____
2.Supply Current：	□ Pass	□ NG , _____
3.Driving Voltage for OLED：	□ Pass	□ NG , _____
4.Contrast for OLED：	□ Pass	□ NG , _____
5.Negative Voltage Output：	□ Pass	□ NG , _____
6.Interface Function：	□ Pass	□ NG , _____
7.ESD test：	□ Pass	□ NG , _____
8.Others ：	□ Pass	□ NG , _____

5、Summary：

Sales signature： _____
Customer Signature： _____   **Date**： ___ / ___ / ___